

**Amendments to the Specification:**

Please replace the paragraph on page 4, lines 19-24 with the following amended paragraph:

A block diagram showing the logical links between a transmitting device and a receiving device over a DVI interface is illustrated in Figure 3. A graphics controller 30 within the transmitting device sends pixel data and control data to a TMDS transmitter 32. The TMDS transmitter 32 then sends data over the ~~DVI interface~~ TMDS links 38 to a TMDS receiver 34. The TMDS receiver 34 then provides pixel data and control data to a display controller 36 within the receiving device.